

Abstract of the Disclosure:

A plurality of digital-analog converters and analog-digital converters are connected in the data lines between the connection contacts and the memory cells of a memory device.

- 5      The memory can be read, written to and actuated by analog data transfers instead of the previous digital signals. The same volume of data, for which a plurality of connection contact areas were normally required, can thus be read via just one connection contact. Addressing the memory cells requires no
- 10     more than respective contact areas for the analog row address and the analog column address, which are converted into digital addresses using analog-digital converters.

WHS:nt - P2001,0274F//10/14/2003